

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	
Steiss)	Art Unit: 2183
)	
Patent No.: 7,206,922)	Examiner: PETRANEK, Jacob A.
)	
Issue Date: April 17, 2007)	Confirmation No.: 1387
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For: INSTRUCTION MEMORY HIERARCHY)	
FOR AN EMBEDDED PROCESSOR)	

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CERTIFICATE OF ELECTRONIC TRANSMISSION UNDER 37 C.F.R. § 1.8			
I hereby certify that this correspondence, including any items indicated as attached or included, is being transmitted via electronic transmission via EFS-Web on the date indicated below.			
Name of Person Signing (Print/Type)	David A. Cornett		
Signature	/David A. Cornett/	Date	May 12, 2008